

## Electronic Patent Application Fee Transmittal

Application Number:	10731714
Filing Date:	09-Dec-2003
Title of Invention:	At-speed ATPG testing and apparatus for SoC designs having multiple clock domain using a VLCT test platform
First Named Inventor:	Anupama Aniruddha Agashe
Filer:	J. Dennis Moore/Shannon Trotter
Attorney Docket Number:	TI-36268

Filed as Large Entity

### **Utility Filing Fees**

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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**Basic Filing:**

**Pages:**

**Claims:**

**Miscellaneous-Filing:**

**Petition:**

**Patent-Appeals-and-Interference:**

Post-Allowance-and-Post-Issuance:

**Extension-of-Time:**

Extension - 1 month with \$0 paid	1251	1	120	120
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Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>120</b>